

FIG.2

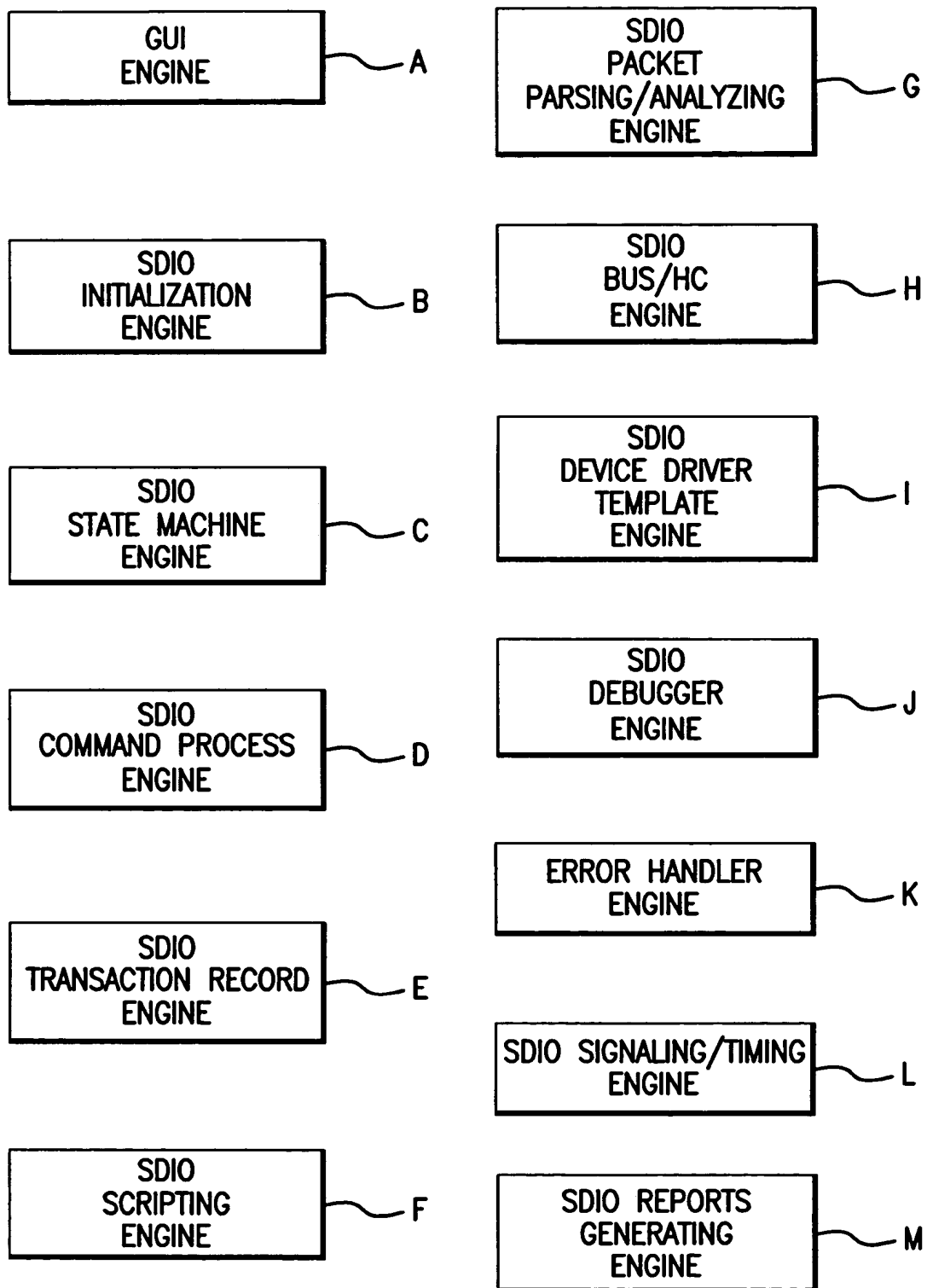
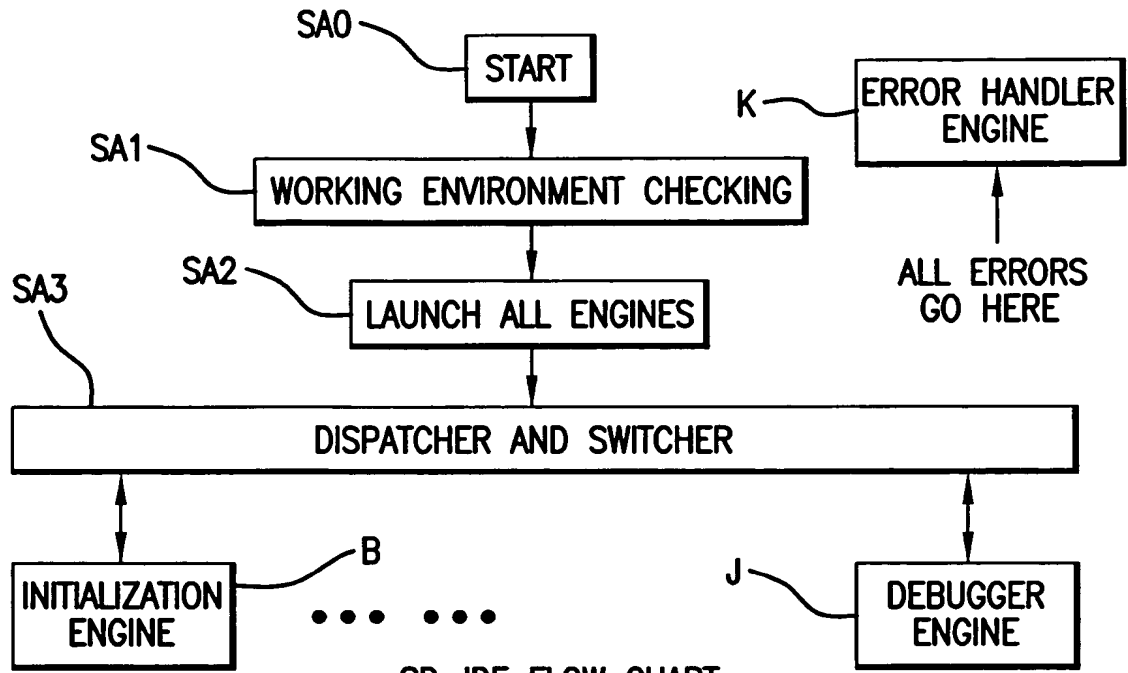
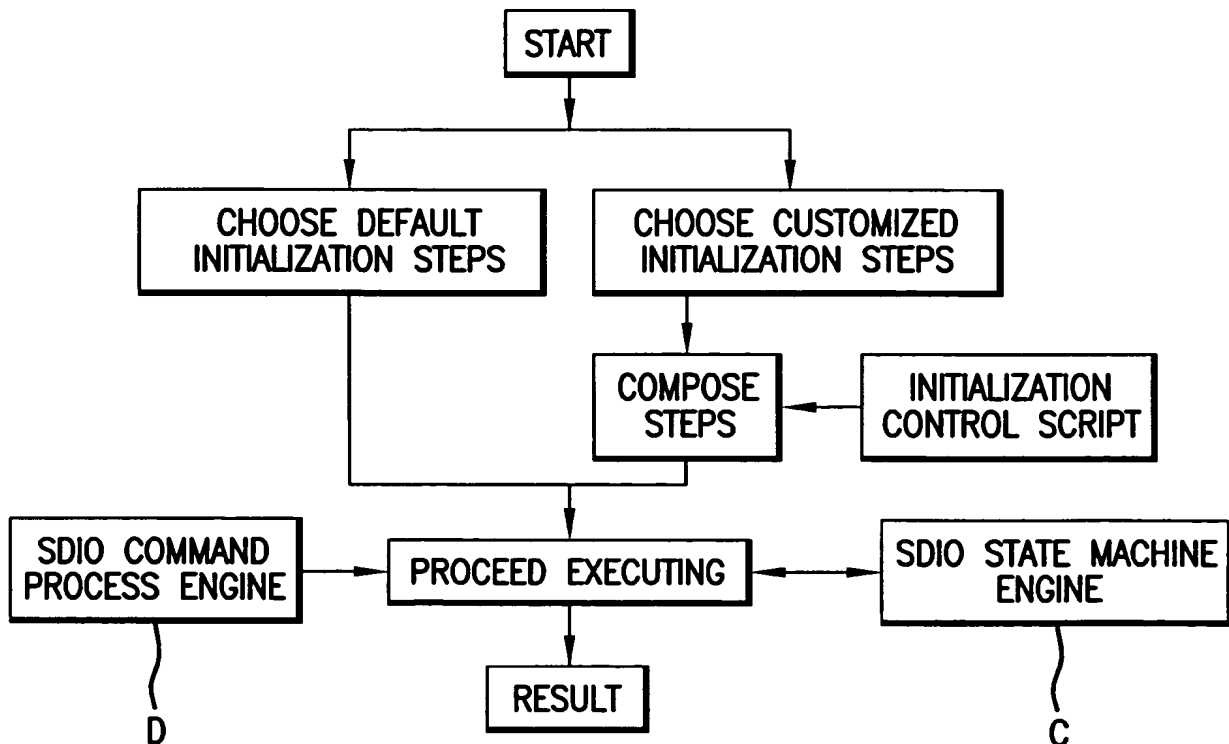


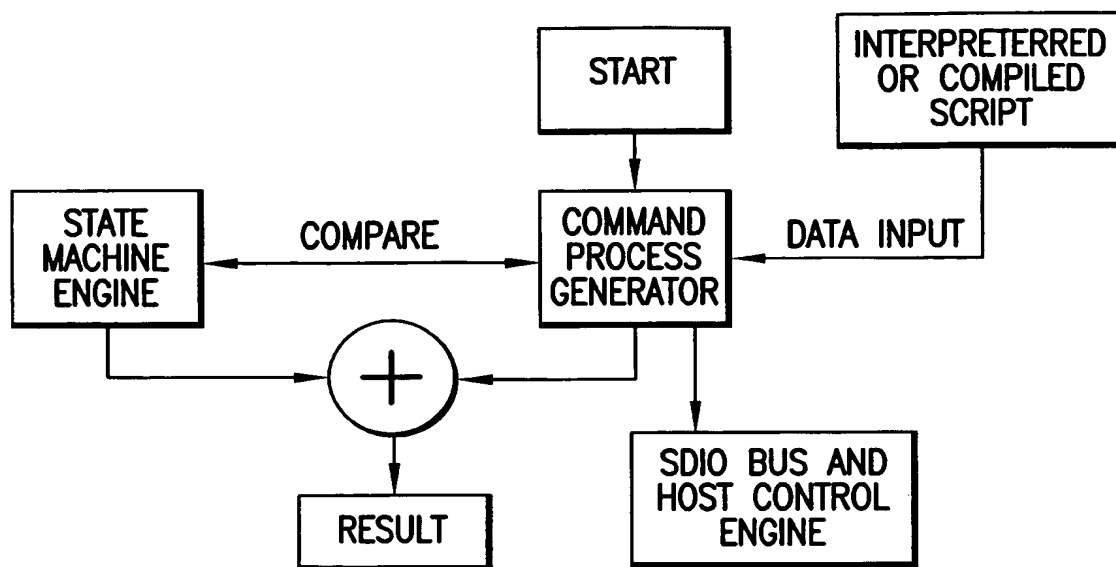
FIG.3



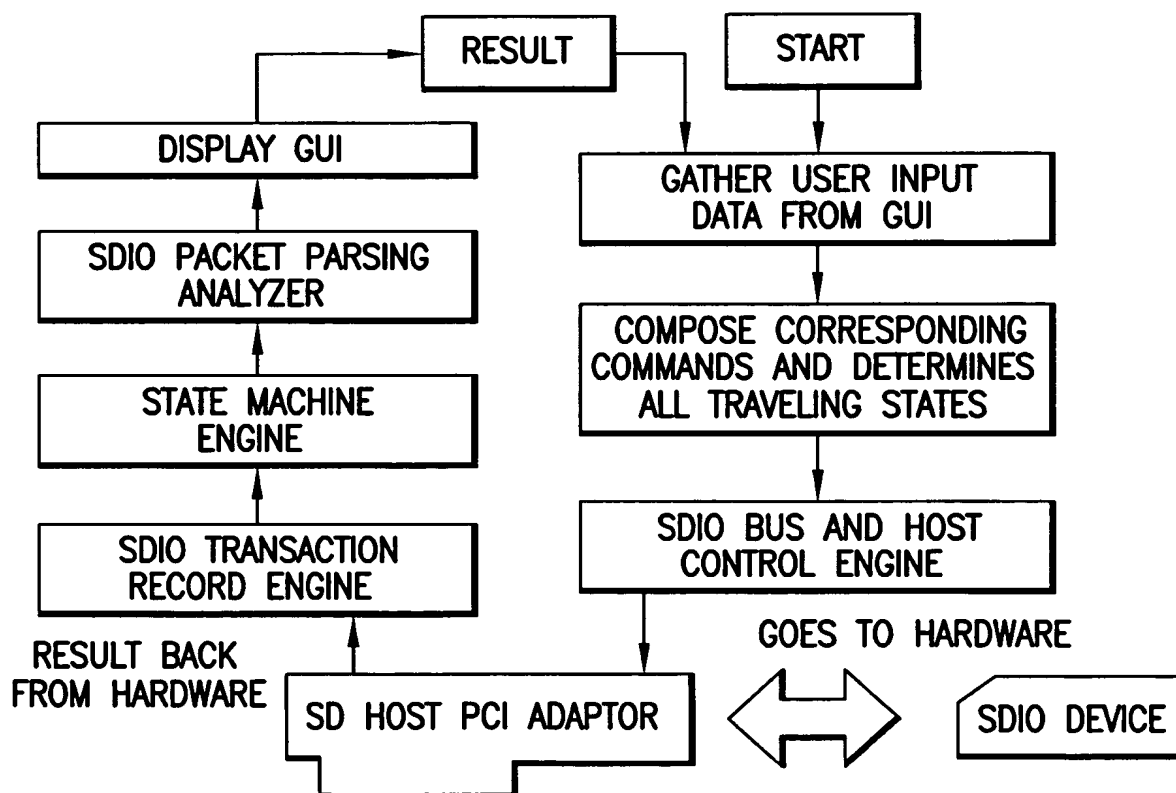
SD-IDE FLOW CHART
FIG.4(a)



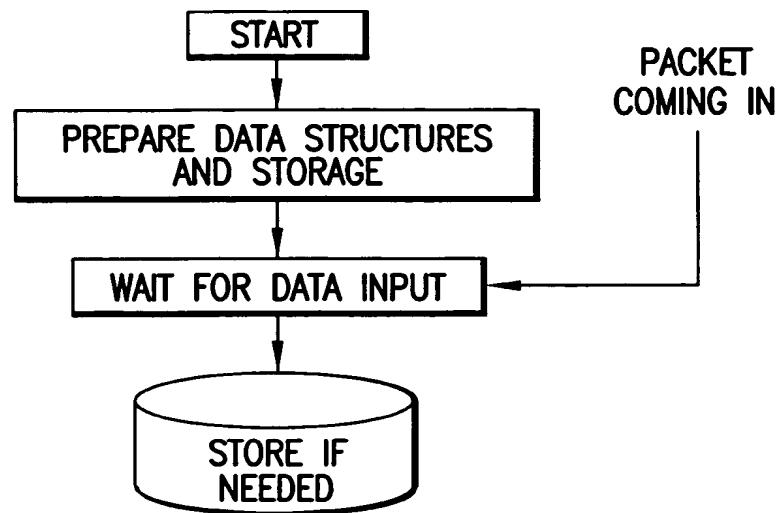
SDIO INITIALIZATION ENGINE
FIG.4(b)



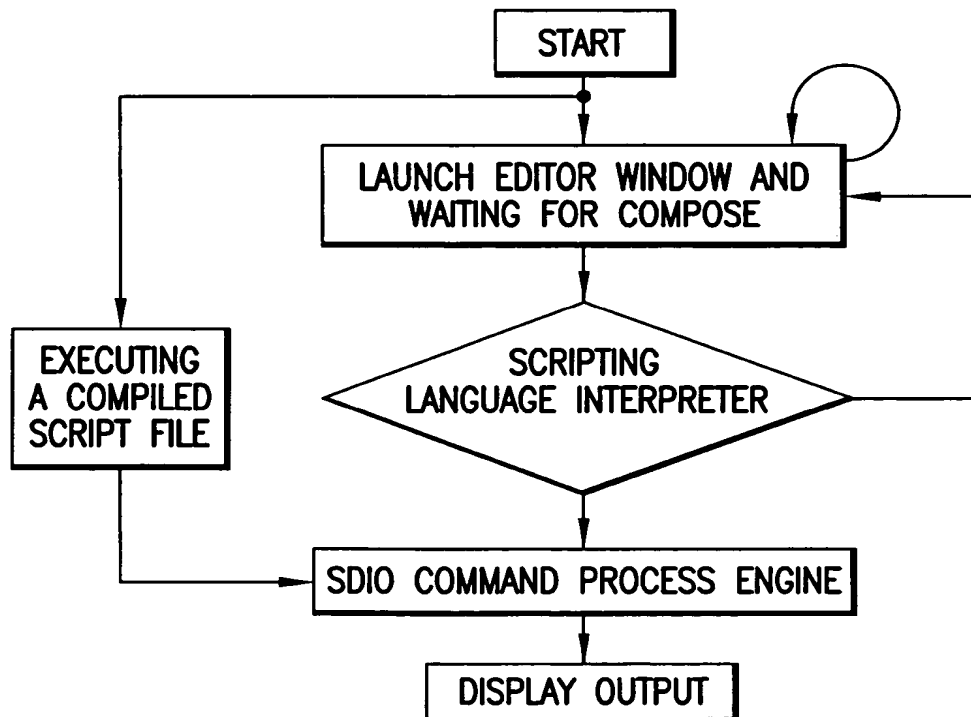
SDIO COMMAND PROCESS ENGINE
FIG.5(a)



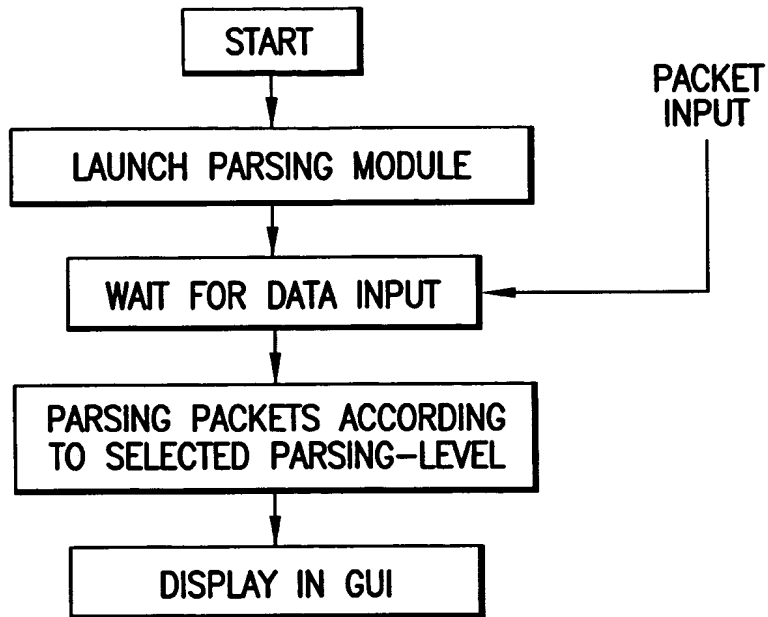
SDIO STATE MACHINE ENGINE
FIG.5(b)



SDIO TRANSACTION RECORD ENGINE
FIG.6(a)

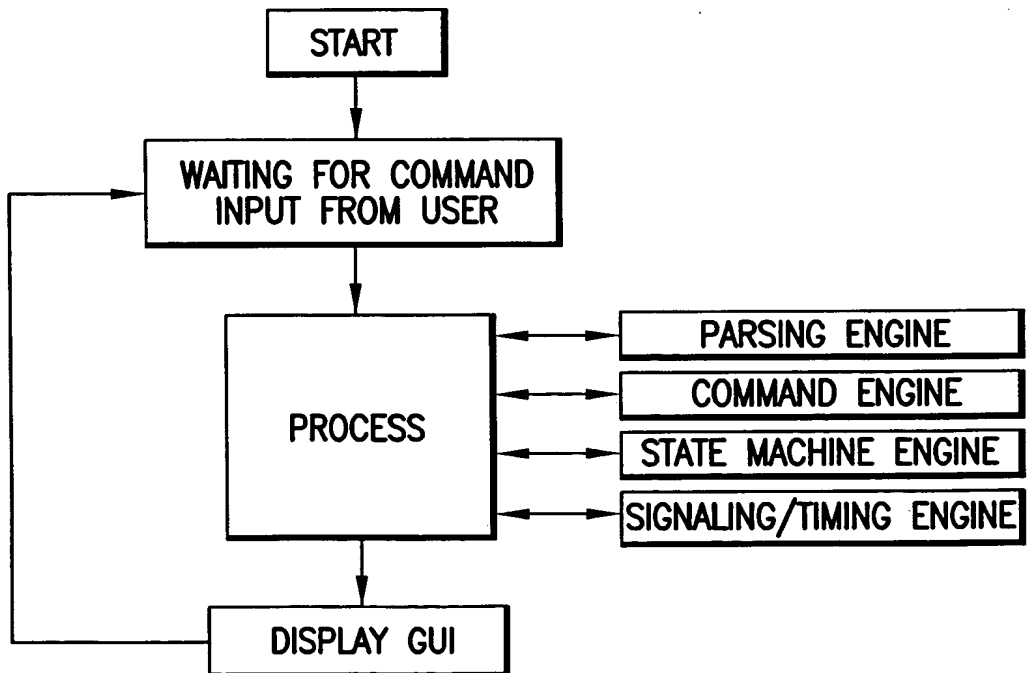


SDIO SCRIPTING ENGINE
FIG.6(b)



SDIO PARSING/ANALYZING ENGINE

FIG. 7(a)



SDIO DEBUGGER ENGINE

FIG. 7(b)